



## Koneru Lakshmaiah Education Foundation

(Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956)

Accredited by NAAC as 'A++' ❖ Approved by AICTE ❖ ISO 21001:2018 Certified

Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhra Pradesh, INDIA.

Phone No. +91 8645 - 350 200; www.klef.ac.in; www.klef.edu.in; www.kluniversity.in

Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

Ref: KLEF/RO/ECE/2019-20

Date: 04-01-2020

### Orders of the Vice-Chancellor dt.04-01-2020

#### CIRCULAR

Sub: Conduction of ZROTRIYA 2020 – A National level Technical Fest – Reg.

Ref: Letter dated 04-01-2020 from Dr.K. Ch. Sri Kavya, Convener-ZROTRIYA forwarded by Dr.M. Suman, HOD-ECE.

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This is by direction to inform that the Department of Electronics & Communication Engineering is conducting “ZROTRIYA 2020” a National level Technical Fest that includes various technical events like workshops, paper presentations, poster presentations and Project Expo.

#### **Programme details:**

Dates : 06th and 07th January 2020

Venue : R&D Block

Invitation and brochure of the Fest are enclosed herewith.

**Encl: Invitation and Brochure**

  
(Dr.R.R.L. KANTHAM)  
Registrar  
**REGISTRAR**



Department of  
Electronics and Communication Engineering  
Presents

**2020**  
**ZROTRIYA**  
A National Level Technical Fest

**ON JANUARY**  
**6<sup>TH</sup> & 7<sup>TH</sup>, 2020**



**#kritril**  
**upayog**



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### “Zrotriya-20”

Department of ECE Technical Fest

#### **Objective:**

The objective of the BUGSPOT event centered around fostering innovation and problem-solving within the realm of system-on-chip (SoC) technology. Participants were tasked with identifying, diagnosing, and resolving bugs or issues within complex SoC designs. Through this challenge, the event aimed to cultivate participants' analytical skills, technical proficiency, and teamwork abilities while promoting creativity and ingenuity in addressing real-world SoC challenges.

**Description:** KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “BUGSPOT” program on 20-03- 2021 at R&D 107(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of Mr. I. Veera Raghava Rao, Ms. C.Priyanka, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

**Organizer:** Pulse - ECE Department Student Body

## **Zrotriya-2020 Events Description:**

### **CIRCUITRIX 2.0**

<b>Date of the Event</b>	: 06-01-2020
<b>Name of the Event</b>	:CIRCUITRIX 2.0
<b>Venue</b>	: R104(Lab)
<b>Event Coordinator</b>	: Dr. P.Syam Sundar

The objective of CIRCUITRIX 2.0 was to provide participants with a platform to showcase their proficiency in electrical circuit design, analysis, and troubleshooting, fostering learning, skill development, collaboration, and inspiration within the field of electrical engineering. The outcomes of CIRCUITRIX 2.0 encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

CIRCUITRIX 2.0 comprised several competitive rounds designed to test participants' capabilities across different areas of electrical engineering. The event featured challenges ranging from basic circuit design to complex troubleshooting scenarios, ensuring a comprehensive assessment of participants' skills.

Participants were required to solve theoretical problems related to electrical circuits, demonstrating their understanding of fundamental concepts.

In this round, participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

Participants used simulation software to analyze and optimize circuit performance under different conditions. This round tested participants' ability to interpret simulation results and make informed design decisions.

This round presented participants with malfunctioning circuits, and they were required to

identify and rectify faults within a stipulated time frame. Effective problem diagnosis, logical reasoning, and swift decision-making were crucial in this round.

Finalists presented their circuit designs, analysis techniques, and problem-solving approaches to a panel of judges. Participants were evaluated based on the clarity of their presentation, depth of understanding, and ability to articulate their ideas effectively.

CIRCUITRIX 2.0 achieved its objective of providing a platform for participants to showcase their skills and knowledge in electrical engineering. The event not only assessed participants' technical proficiency but also encouraged collaboration, as participants exchanged ideas and strategies throughout the competition.

Moreover, CIRCUITRIX 2.0 fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

CIRCUITRIX 2.0, as part of “**Zrotriya**” played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges. Moving forward, initiatives like CIRCUITRIX 2.0 are essential for nurturing the next generation of electrical engineers and driving technological advancement in the field.

### **NI MyRioWORKSHOP**

**Date of the Event** : 07-01-2020

**Name of the Event** : NI MyRioWORKSHOP

**Venue** : R-6<sup>th</sup> FLOOR (Lab)(COE)

**Event Coordinator** : Dr.P.Pardha saradhi

An NI MyRio workshop aims to equip participants with the skills to utilize National Instruments' MyRio data acquisition system. The workshop likely provides a hands-on approach, familiarizing attendees with the MyRio hardware and software, enabling them to configure the system for different data acquisition tasks and effectively interpret the collected measurements.

## Outcome:

An NI MyDaq workshop offers a valuable learning experience for those interested in data acquisition. Through the workshop, you'll gain hands-on skills in using National Instruments' MyDaq system. This includes learning how to configure the hardware and software to effectively collect data from various sensors and instruments. You'll also delve into methods for analyzing the acquired measurements, allowing you to extract meaningful insights from the data. By the workshop's conclusion, you'll be well-equipped to utilize the MyDaq system for your own data acquisition projects.

The Zrotriya National Level Technical Fest celebrated the realm of electronics with its flagship event titled "Advancements in Electronics: A Glimpse into the Future." This event was meticulously organized by the Electronics Branch of the fest, aiming to provide participants with insights into cutting-edge developments in electronic technologies.

**Event Overview:** The event comprised various segments tailored to cater to the diverse interests and knowledge levels of participants. From keynote presentations by industry experts to hands-on workshops and competitive challenges, the event encapsulated the essence of contemporary electronic innovations.

1. **Keynote Presentations:** Renowned experts from academia and industry delivered keynote addresses, shedding light on emerging trends and future prospects in electronics. Topics ranged from quantum computing and nanotechnology to Internet of Things (IoT) and artificial intelligence (AI) applications in electronics
2. **Workshops:** Practical workshops were conducted to impart valuable skills and knowledge to participants. Topics included PCB design, embedded systems programming, robotic automation, and 3D printing in electronics manufacturing. These workshops provided attendees with the opportunity to delve into hands-on learning experiences under the guidance of experienced instructors.
3. **Technical Competitions:** The event hosted a series of technical competitions designed to challenge participants' ingenuity and problem-solving abilities. Contests such as circuit design challenges, robotics competitions, and hackathons tested participants' creativity and technical prowess. Winners were rewarded with accolades and prizes, motivating participants to showcase their best efforts.
4. **Exhibition:** An exhibition showcasing the latest electronic gadgets, prototypes, and

research projects was a major attraction of the event. Participants had the opportunity to interact with exhibitors, explore innovative technologies, and gain insights into real-world applications of electronics.

5. Panel Discussions: Engaging panel discussions were organized on pertinent topics such as sustainable electronics, ethical considerations in technology development, and the future of consumer electronics. These discussions fostered intellectual discourse and encouraged participants to critically analyze the societal impact of electronic advancements. Conclusion: "Advancements in Electronics: A Glimpse into the Future" emerged as a resounding success, captivating the imagination of participants and fostering a spirit of innovation and collaboration. The event not only showcased the latest developments in electronics but also provided a platform for networking, learning, and inspiration.

As technology continues to evolve at a rapid pace, events like these play a pivotal role in shaping the future of electronic engineering and fostering a vibrant ecosystem of technological innovation. Acknowledgments: The success of this event would not have been possible without the unwavering support of the organizing committee, volunteers, sponsors, speakers, and participants.

Their collective efforts and enthusiasm contributed to making this event a memorable and enriching experience for all involved. Future Prospects: As the field of electronics continues to evolve, the Zrotriya National Level Technical Fest is committed to organizing more such events to keep pace with the dynamic nature of technology and provide a platform for fostering creativity, collaboration, and excellence in the field of electronics engineering.

### **POSTER PRESENTATION**

Date of the Event : 06-01-2020  
Name of the Event : POSTER PRESENTATION  
Venue : R104(Lab)  
Event Coordinator : B Sai Sandeep, G L P Ashok

Zrotriya, a prestigious national-level techno fest, organized by Team Zrotriya, hosted an exhilarating event centered around poster designing. The event aimed to provide a platform for participants to showcase their creativity, innovation, and design skills through visually captivating posters. The Poster Presentation event not only celebrated

artistic expression but also encouraged participants to communicate complex ideas effectively through graphic design.

The primary objective of the Poster Presentation event was to foster creativity and innovation among participants while emphasizing the importance of visual communication in conveying technical concepts. By challenging participants to design posters on diverse themes related to science, technology, engineering, and mathematics (STEM), the event aimed to promote interdisciplinary collaboration and inspire novel approaches to problem-solving.

KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “POSTER PRESENTATION” program on 20-03-2021 at R&D 104(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of B Sai Sandeep,G L P Ashok, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of “POSTER PRESENTATION” encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

JUDGES :

- 1) Dr K. Srinivasa Rao
- 2) Dr.Phani Kishore 3)Dr.Rehman 4)Dr.K.S.Ramesh

Event Structure:

POSTER PRESENTATION comprised several competitive rounds designed to test participants' capabilities across different areas of electrical engineering. The event featured challenges ranging from basic circuit design to complex troubleshooting



scenarios, ensuring a comprehensive assessment of participants' skills.

**Preliminary Round:** Participants were required to solve theoretical problems related to electrical circuits, demonstrating their understanding of fundamental concepts.

**Design Challenge:** In this round, participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

**Simulation Round:** Participants used simulation software to analyze and optimize circuit performance under different conditions. This round tested participants' ability to interpret simulation results and make informed design decisions.

**Troubleshooting Round:** This round presented participants with malfunctioning circuits, and they were required to identify and rectify faults within a stipulated time frame. Effective problem diagnosis, logical reasoning, and swift decision-making were crucial in this round.

**Presentation Round:** Finalists presented their circuit designs, analysis techniques, and problem-solving approaches to a panel of judges. Participants were evaluated based on the clarity of their presentation, depth of understanding, and ability to articulate their ideas effectively.

**Outcome:**

POSTER PRESENTATION achieved its objective of providing a platform for participants to showcase their skills and knowledge in electrical engineering. The event not only assessed participants' technical proficiency but also encouraged collaboration, as participants exchanged ideas and strategies throughout the competition.

Moreover, POSTER PRESENTATION fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

**Conclusion:**

POSTER PRESENTATION, as part of “Zrotriya” played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development,

and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges. Moving forward, initiatives like POSTER PRESENTATION are essential for

### **BUGSPOT**

Date of the Event : 06-01-2020  
Name of the Event : BUGSPOT  
Venue : R304  
Event Coordinator : Dr.N.Prabhakaran

Pulse, the student body of the Electronics and Communication Engineering (ECE) department, orchestrated an innovative event titled "BUGSPOT" during the Zrotriya National Level Technical Fest. This event was meticulously crafted with the objective of fostering innovation and problem-solving within the realm of System-on-Chip (SoC) technology.

BUGSPOT aimed to challenge participants' analytical skills, technical proficiency, and teamwork abilities through the identification, diagnosis, and resolution of bugs or issues within complex SoC designs. By delving into real-world SoC challenges, the event sought to cultivate creativity and ingenuity among participants while providing a platform for collaborative problem-solving.

Participants were presented with intricate SoC designs containing hidden bugs or issues. Their task was to meticulously analyze the designs, identify the anomalies, and devise effective solutions to rectify the detected bugs. The challenge format encouraged participants to employ a combination of theoretical knowledge, practical expertise, and innovative thinking to overcome obstacles.

The event emphasized the importance of teamwork, as participants worked collaboratively in teams to unravel the complexities of SoC designs. By fostering interdisciplinary collaboration and communication, BUGSPOT encouraged participants to leverage each other's strengths and perspectives in tackling challenging scenarios.

BUGSPOT provided participants with a platform to showcase their technical prowess in

SoC design and debugging. Participants utilized state-of-the-art tools and methodologies to analyze and debug complex hardware and software interactions within SoC architectures.

Participants were encouraged to think outside the box and explore innovative solutions to address SoC bugs effectively. Through creative problem-solving approaches, participants demonstrated their ability to adapt to dynamic challenges and devise novel strategies for bug resolution. BUGSPOT served as a catalyst for cultivating a culture of innovation and ingenuity within the ECE community.

BUGSPOT emerged as a captivating and intellectually stimulating event, showcasing the prowess of participants in tackling real-world SoC challenges. The event not only honed participants' analytical skills and technical proficiency but also fostered a spirit of teamwork, innovation, and creativity within the ECE community. As technology continues to advance, events like BUGSPOT play a crucial role in preparing future engineers to tackle the complexities of SoC design and contribute meaningfully to the field of electronics engineering.

The success of BUGSPOT would not have been possible without the dedicated efforts of Pulse, the ECE department student body, as well as the enthusiastic participation of all attendees. Their collective commitment to excellence and passion for innovation contributed to making BUGSPOT a memorable and enriching experience for all involved.

Moving forward, Pulse is committed to organizing more such events that challenge participants to push the boundaries of innovation and problem-solving in electronic engineering. BUGSPOT has set a precedent for fostering collaboration, creativity, and technical excellence within the ECE community, and future editions of the event are poised to build upon this foundation and inspire the next generation of electronic engineers.

The outcomes of the BUGSPOT event culminated in the successful identification and resolution of bugs within various system-on-chip (SoC) designs, showcasing participants' adeptness in problem-solving and technical proficiency. Participants gained valuable hands-on experience in diagnosing and addressing complex SoC issues, furthering their skills and knowledge in this specialized field. Moreover, the event fostered collaboration and teamwork among participants, promoting the exchange of ideas and best practices. The successful resolution of bugs not only contributed to the advancement of SoC

technology but also provided participants with a sense of accomplishment and validation of their capabilities within the field.

### **PROJECT EXPO**

**Date of the Event** : 07-01-2020  
**Name of the Event** : PROJECT EXPO  
**Venue** : R104(Lab)  
**Event Coordinator** : Dr.D.Bhavana

Zrotriya, a prestigious national-level techno fest, organized by Team Zrotriya, hosted an exhilarating event centered around poster designing. The event aimed to provide a platform for participants to showcase their creativity, innovation, and design skills through visually captivating posters. PROJECT EXPO event not only celebrated artistic expression but also encouraged participants to communicate complex ideas effectively through graphic design.

Objective:

The primary objective of PROJECT EXPO was to offer participants an opportunity to present their innovative projects and research endeavors to a wider audience. The event aimed to encourage knowledge sharing, inspire creativity, and recognize outstanding achievements in various fields of technology. Additionally, PROJECT EXPO aimed to promote interdisciplinary collaboration and facilitate networking among participants, industry professionals, and academia.

KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “PROJECT EXPO ” program on 22-03-2024 at R&D 104(LAB). We started the event at 9:30AM.

With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements. Under the guidance of K.Sriathi Roy this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of “PROJECT EXPO ” encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the

enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

By challenging participants to design posters on diverse themes related to science, technology, engineering, and mathematics (STEM), the event aimed to promote interdisciplinary collaboration and inspire novel approaches to problem-solving.

#### Event Structure:

Project Expo featured an array of innovative projects spanning multiple disciplines, including but not limited to computer science, engineering, biotechnology, and sustainability. The event comprised the following components:

#### Project Exhibits:

Participants set up booths to showcase their projects, providing detailed demonstrations and explanations to visitors. The exhibits offered a hands-on experience, allowing attendees to interact with the projects and understand their functionalities.

#### PROJECT EXPO:

In addition to physical exhibits, participants prepared posters summarizing their projects' objectives, methodologies, and outcomes. PROJECT EXPO s provided a concise overview of the projects and facilitated discussions among participants and visitors. participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

#### Technical Talks and Workshops:

Project Expo featured technical talks and workshops conducted by industry experts and academic scholars. Topics covered a wide range of subjects, including emerging technologies, research methodologies, and career opportunities in various fields.

#### Judging and Awards Ceremony:

A panel of esteemed judges evaluated the projects based on criteria such as innovation, technical merit, practical applicability, and presentation quality. Winners were recognized

and awarded prizes during the closing ceremony, acknowledging their contributions to the technological landscape.

#### Outcomes:

Project Expo at Zrotriya facilitated knowledge exchange, collaboration, and inspiration among participants and attendees. The event provided a platform for emerging talents to gain visibility, receive feedback, and forge valuable connections within the tech community. Moreover, Project Expo promoted innovation and encouraged participants to explore new frontiers in technology, addressing real-world challenges and driving positive change.

By showcasing a diverse range of projects, Project Expo highlighted the significance of interdisciplinary collaboration and the transformative power of technology in addressing global issues. Participants gained invaluable experience in project management, communication, and problem-solving, fostering their personal and professional development.

Project Expo emerged as a cornerstone event within Zrotriya, encapsulating the essence of innovation, collaboration, and excellence in technology. The event not only celebrated the achievements of participants but also inspired future generations to pursue their passion for technology and make meaningful contributions to society. Moving forward, initiatives like Project Expo are instrumental in nurturing talent, fostering innovation, and shaping the future of technology on a global scale.

Moreover, PROJECT EXPO fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

Zrotriya's PROJECT EXPO Event: A dynamic showcase of creativity and innovation, where participants craft visually stunning posters to communicate complex STEM concepts effectively. Empowering participants to unleash their design talents and express their ideas visually through PROJECT EXPO event at the national-level techno fest, Zrotriya

#### Conclusion:

PROJECT EXPO, as part of "Zrotriya" played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation.

## **TI WorkShop Venue**

**Date of the Event** : 07-01-2020

**Name of the Event** : TI WorkShop

**Venue** : R104(Lab)

**Event Coordinator** :

The objective of TI WORKSHOP was to provide participants with a platform to showcase their proficiency in electrical circuit design, analysis, and troubleshooting, fostering learning, skill development, collaboration, and inspiration within the field of electrical engineering.

Description: KL Deemed to be University (Koneru Lakshmaiah Education Foundation) Department of ECE conducted “TI WORKSHOP” program on 20- 04-2024 at R&D 104(LAB). We started the event at 9:30AM. With the intention of educating the students regarding basic electronics, Students are very eagerly learning and were very interested in taking part in their given problem statements.

Under the guidance of N.Durga Indira, Y.Usha Devi, this event was very successful. Mr. P. Srikanth Reddy (Program Coordinator, KLEF PULSE) also took part in the event and together made the event a grand success.

The outcomes of TI WORKSHOP encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering

### **Event Structure:**

TI WORKSHOP comprised several competitive rounds designed to test participants' capabilities across different areas of electrical engineering. The event featured challenges ranging from basic circuit design to complex troubleshooting scenarios, ensuring a comprehensive assessment of participants' skills.

Preliminary Round: Participants were required to solve theoretical problems related to

electrical circuits, demonstrating their understanding of fundamental concepts.

**Design Challenge:** In this round, participants were tasked with designing circuits to meet specific requirements, such as voltage regulation, signal amplification, or frequency filtering. Creativity, efficiency, and adherence to design constraints were key evaluation criteria.

**Simulation Round:** Participants used simulation software to analyze and optimize circuit performance under different conditions. This round tested participants' ability to interpret simulation results and make informed design decisions.

**Troubleshooting Round:** This round presented participants with malfunctioning circuits, and they were required to identify and rectify faults within a stipulated time frame. Effective problem diagnosis, logical reasoning, and swift decision-making were crucial in this round.

**Presentation Round:** Finalists presented their circuit designs, analysis techniques, and problem-solving approaches to a panel of judges. Participants were evaluated based on the clarity of their presentation, depth of understanding, and ability to articulate their ideas effectively.

TI WORKSHOP achieved its objective of providing a platform for participants to showcase their skills and knowledge in electrical engineering. The event not only assessed participants' technical proficiency but also encouraged collaboration, as participants exchanged ideas and strategies throughout the competition.

Moreover, TI WORKSHOP fostered a spirit of innovation and creativity by challenging participants to devise efficient solutions to real-world problems. Participants gained valuable insights into the practical application of electrical engineering principles and developed critical thinking skills essential for the field.

**Conclusion:**

TI WORKSHOP, as part of “Zrotriya” played a pivotal role in promoting excellence in electrical engineering by nurturing talent, encouraging skill development, and fostering a culture of innovation. The event provided participants with a platform to demonstrate their abilities, collaborate with peers, and gain invaluable experience in tackling real-world engineering challenges. Moving forward, initiatives like TI WORKSHOP are essential for nurturing the next generation of electrical engineers and driving technological advancement in the field.



**Outcome:**

ZROTRIYA-2020, meticulously crafted as a technical fest, yielded resounding success, leaving an enduring impact on its participants and stakeholders. The fest ignited a wave of innovation, inspiring participants to explore novel ideas and solutions within the realm of Electronics & Communication Engineering (ECE). Through collaborative initiatives and interdisciplinary projects, participants harnessed the collective expertise of the ECE community, fostering meaningful engagement and collaboration. Empowered by hands-on workshops and technical talks, participants emerged with enhanced technical skills, poised to tackle complex challenges and contribute meaningfully to the field. ZROTRIYA-2020 served as a vital bridge between academia and industry, providing invaluable exposure to industry trends, career opportunities, and collaborative ventures. By celebrating excellence and nurturing an entrepreneurial spirit, the fest inspired participants to strive for greatness and set new benchmarks in ECE. Beyond its immediate impact, ZROTRIYA-2020 left a legacy of learning and inspiration, propelling participants on a trajectory of continuous growth and professional development in the field of ECE.

The outcomes of “PAPER PRESENTATION” encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

The outcomes of “PROJECT EXPO” encompassed the recognition of winners for their prowess in electrical circuit design, analysis, and troubleshooting, alongside the enhancement of participants' skills and knowledge in the field. Networking opportunities facilitated valuable connections among enthusiasts, students, educators, and professionals, fostering collaboration and knowledge exchange. Feedback collected from stakeholders aimed at refining future editions, ensuring continual improvement. Moreover, the event served as a source of inspiration and motivation, encouraging participants to pursue further exploration and opportunities within the realm of electrical engineering.

## Event Photos



faculty members were involved in this event and they came front to interview and conducted a HR round for the students.



Students involved actively in BUGSPOT Event



event Zrotriya -2020 presentation at R & D Block



Participants of project expo



**students as a group as their wish and give a troubleshoot to solve it**



Project Expo a part of Zrotriya -2020



**Participants of Paper Presentations**



**Participation of Students in BUGSPOT**

Registration Link :

<https://forms.office.com/pages/responsepage.aspx?id=PsiMgEal50egP3Oh67ok8yLwvXoPAQBMrKbLikkQTQxUQzJNSzVBWUNUTE9PWUc3N1cyRkMIT0VMVi4u>

**Participant's list**

1	CHAMARTHI VENKATA SAI KARTHIK	180040457	10	Sai Kartik
2	Jonnalagadda Sree Harshitha	180040460	10	Harshitha
3	NIMMAGADDA. LAKSHMI SOUNDARIYA	180040470	10	N. Lakshmi
4	V.Mohana vamsi	180040474	10	vamsi
5	Amruthavani Bhumireddy	180040498	10	Bhumireddy
6	nagalla shiva	180040499	10	shiva
7	DEVARASHETTY NIKITH	180040513	10	Nikith
8	K.S.V.Shanmukha Priya	180040542	10	Shanmukha
9	Sk lubna kowsar	180040556	10	Kowsar
10	yeddula.vamsidhar reddy	180040559	10	vamsidhar
11	K.S.V.R.Kumar	180040570	10	Kumar
12	T. Bala Vamsi	180040575	10	vamsi
13	SIDDINENI POOJA NAIDU	180040576	10	S. Pooja
14	D VEERAJANARDHANA ACHARI	180040577	10	Achari
15	pisini pradeep	180040583	10	pradeep
16	Pokala satyanarayana	180040587	10	Satyanarayana
17	MALLEPULA DURGA DHEERAJ	180040595	10	Dheeraj
18	ANNEM AKASH	180040596	10	Akash
19	kolli nagasree	180040600	10	Nagasree
20	Vasireddy Balasaraswathi	180040604	10	Bala
21	Peram Hanvitha	180040608	10	Hanvitha
22	NAGA TEJA	180040613	10	Naga Teja
23	lakshmi priyanka. palapati	180040616	10	P. Lakshmi
24	DammalapatiSriPrathyusha	180040619	10	Prathyusha
25	Rohit Bonigala	180040633	10	Rohit
26	Chandana Dudam	180040644	10	Dudam
27	Chintapoodi pavankalyan	180040645	10	Pavankalyan
28	P.sailesh chowdary	180040658	10	Chowdary
29	G.vaishnavi	180040662	10	Vaishnavi
30	P SAI CHARAN	180040666	10	Charan
31	Dinesh Vardhan	180040667	10	Dinesh
32	V V N A Vishal	180040668	10	Vishal
33	KARISHMA BEGUM	180040669	10	K. Begum
34	Shaik Sameera Farheen	180040672	10	Farheen
35	Solasu charan	180040683	10	Charan
36	BELLAM VARUN KUMAR	180040689	10	Varun
37	Naveen Kalidindi	180040695	10	Kalidindi
38	KURUGUNTLA TANUJA	180040698	10	Tanuja
39	DAGGUPATI HARIPRASAD CHOWDARY	180040707	10	Hariprasad
40	T.Fakrunnisa Begum	180040708	10	Fakrunnisa
41	peddiboinasrilaxmi	180040710	10	Srilakshmi
42	I.Narendra Datta	180040736	10	Narendra
43	Venne Ramya	190040564	10	V. Ramya
44	A.Chandra Haneesh	170040044	10	Haneesh
45	A.Sai Kiran	170040047	10	Sai Kiran
46	A.Durga Sahithi	170040054	10	Sahithi

JANISIRI GOPALAKRISHNA	170180017	12	Gopal Krishna
JEEMAJAMES KABANYWANYI	170180018	12	Kabanywanyi
DRISADAM ISMAIL	170180020	12	Ismael
AHMUDIDRIS	170180021	12	Mahmud
NGRID DISMAS ASSEY	170180023	12	Ngri
IESTORY MBILINYI	170180024	12	N. Mbilinyi
ODWIN ANTELM ANDREW	170180025	12	Andrew
ABIOLA CLEMENT UMBU	170180026	12	Clement
RENE LAZARO NGWETIAMA	170180027	12	Lazaro
Ad ABDALLA MOHAMED ABDELWAHAB	170180029	12	Mohamed
HALIL ASIMABDALLA GABIR	170180031	12	Gabir
SHISH KUMAR SHARMA	180180001	12	Sharma
INDU BINDU SAI LAKSHMI	180180002	12	Bindu
PRUDHVI KESHAVA	180180003	12	Prudhvi
DEVOJEET SARKAR	180180004	12	Sarkar
SADDE SRIVALLI	180180005	12	S. Srivalli
INDU BINDU HASINI	180180006	12	Hasini
TIPPESWAMY	180180007	12	Tippeswamy
CHANDU VARDHAN	180180008	12	Chandu
RIKKILINENI DHEDEEPPYA	180180009	12	Dhe Deepya
GAGANA SRI	180180010	12	Gagan
CHAITANYA KUMAR	180180011	12	Chaitanya
APARNA REDDY	180180012	12	Aparna
ELAGA INDU	180180013	12	Indu
OLLIPARA VAISHNAVI	180180014	12	Vaishnavi
AINAVARAPU.SUMAKSHARIKA	180180015	12	N. Sumaksharika
mansnehi	180180016	12	Snehi
MBOJI PRAVEEN KUMAR	180180017	12	Praveen
ELVIN MURITHI MUTHENGI	180180018	12	Murithi
MAR TAREQ HASEEB ALDABBAGH	180180019	12	Haseeb
OACHIM ANATOLY SULLE	180180020	12	Anatoly
ALIM ABDALLAH SEIF HAMAD ALZAKWAN	180180021	12	Hamad
ACKLINE (JACQUELINE) JOHN KIDUMBA	180180022	12	John
DILI ZACHEO MSELUKA	180180023	12	Mseluka
HMED MOHAMED YOUSIF WADI	180180024	12	Mohamed Yousef
MMANUEL KWIZERA	180180025	12	E. Kwizera
MOHAMMED ABDALLAH JAMEELALLAH	180180026	12	Abdallah
ICHIMUNYA HANJALIKA	180180027	12	Hanjalka
RESOR KAYIRANGA	180180028	12	Resor
NWER ABDULHALIM MUSSA EDRESS	180180029	12	Mussa Edress
ALIK YAHIA ABDULRASOOL HUSSEIN	180180030	12	Yahia Hussein
OSEE CONSCIENCE UFITSE ARATETA	180180031	12	Osee
CHAITANYA	180180032	12	Chaitanya
BHANU PRAKASH	180180033	12	Bhanu Prakash

Singaraju Siva Surya Kiran	180040092	ECE	S. Singaraju
Mutta Venkata Sai Deepak	180040094	ECE	Deepak
Nadimpalli Sai Kiran	180040096	ECE	N. Sai Kiran
Akula said Bala Siva jyothika	180040099	ECE	
panchayutula sonali	180040104	ECE	sonali
A.Narendra Reddy	180040106	ECE	P. Reddy
Gunnam Purna Chandrika	180040108	ECE	G. Purna
Venkat Namana	180040111	ECE	V. Namana
Dadi Sri Vandhana	180040114	ECE	vothorol
TANNEERU SAI BHARGAV	180040146	ECE	Sai Bhargav
M.Namratha	180040148	ECE	Namratha
N.Laya Sree	180040156	ECE	Laya Sree
Hemchand Pidikiti	180040157	ECE	P. Hemachand
Dalali Arif	180040165	ECE	
Gembali Durga Narasimha Rahul	180040172	ECE	Rahulu
Stalin Raj Kusuma	180040173	ECE	P. Stalin
shaik mohammed junaid	180040176	ECE	M. Junaid
B.Ramcharan Teja	180040179	ECE	B. Teja
K.Sravani Annapurna	180040189	ECE	A. Annapurna
Galla.yamini Lakshmi	180040190	ECE	Lakshmi
Vyshnavi	180040192	ECE	Vyshnavi
Bhavya Tejaswi Manepalli	180040196	ECE	Bhavya Tejaswi
PAILA ANIL SAI JASWANTH	180040198	ECE	P. Anil Sai jaswanti
SANIKOMMU YOGENDHRAAREDDY	180040199	ECE	S. Yogendra
PATRI SAI SREEHITH	180040205	ECE	S. Sreehith
asupuleti.srija	180040228	ECE	P. Srija
3EDDADA SAI SANDEEP	180040229	ECE	S. Sandeep
Radikamalla V V R N Sri Harsha	180040231	ECE	T. V. R. N. Sri Harsha
Mudigonda Vamsi Jwala Ramalingeswar	180040239	ECE	V. Jwala
VANDADI NIKHILA	180040244	ECE	Nikhila
Maddula Tejasri	180040249	ECE	T. Tejasri
ADITYA GOKUL REDDY BHIMAVARAPU	180040254	ECE	A. Aditya
C.Nithya	180040270	ECE	R. Nithya
iemali sri nithya	180040271	ECE	
ripathi.gowthami	180040273	ECE	gowthami
ai chaitanya.Nandina	180040275	ECE	P. Nandina
ahnavi katte	180040283	ECE	A. Ahnavi
odali sai sivani	180040290	ECE	F. Sai Sivani
..Jyothika	180040301	ECE	Jyothika
HAIK ISMAIL BASHA	180040312	ECE	Basha
i.KRISHNAPAVAN	180040318	ECE	Krishnapavan
..harsha	180040320	ECE	H. Harsha
ODAPATI SAI KRISHNA	180040332	ECE	B. Sai Krishna

  
 Head of the Department